IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

KLIESNER ET AL

Serial No. 10/620,151

Filing Date: 07/15/2003

Confirmation No. 8500

For: DIGITAL CLOCK RECOVERY CIRCUIT)

EMPLOYING FIXED CLOCK OSCILLATOR

DRIVING FRACTIONAL DELAY LINE

AMENDMENT

EFILED

Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450

Sir:

In reply to the Office Action dated September 29, 2006, the following remarks are respectfully submitted in connection with the above-identified application.
